


REMARKS

With entry of this amendment, claims 34-69 are pending. Claims 34-69 do not present new matter. See, e.g., original claims 10 and 11; p. 7, line 1 - p. 8, line 3; p. 12, line 17 (configured as target or initiator after layout is finalized); p. 14, line 20 (known systems adhere to specification from component library) and p. 15, line 11 (without adhering to specification); p. 16, line 12 (blocks organized to minimize IC footprint); p. 16, line 16 (adapted to perform as target or initiator based on type of interface for the block connected to the interface); p. 17, line 3 (register to enable setting interface as target or initiator); p. 17, line 8 (logic synthesis operation); p. 23, line 5 (dynamically change); p. 23, line 17 (switch interface types, from initiator to target and vice versa); p. 24, line 14 (bi-directional). Applicants respectfully request reconsideration and allowance of this application. If the Examiner has any questions or comments regarding this amendment, the Examiner is respectfully requested to contact the undersigned.

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